

JCA 946

Class	Subclass
ISSUE CLASSIFICATION	

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/884675		714		2-184	

APPlicants

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TITLE

Hardware efficient handling of instruction exceptions to limit adverse impact on performance

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED	
		(Assistant Examiner) _____ (Date)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____					ISSUE FEE	
		(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					ISSUE BATCH NUMBER	
		(Legal Instruments Examiner) _____ (Date)				

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